



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Noriko SHINOMIYA

Serial No.: 10/606,283

Filed: June 26, 2003

For: SEMICONDUCTOR INTEGRATED CIRCUIT DESIGNING APPARATUS,
SEMICONDUCTOR INTEGRATED CIRCUIT DESIGNING METHOD,
SEMICONDUCTOR INTEGRATED CIRCUIT MANUFACTURING METHOD, AND
READABLE RECORDING MEDIA

SUBMISSION OF ORIGINAL EXECUTED DECLARATION

Mail Stop Missing Parts
Director of United States Patent
and Trademark Office
P.O. Box 1450
Alexandria, VA 22313-1450

PLEASE ACCEPT THIS AS
AUTHORIZATION TO DEBIT
OR CREDIT FEES TO
DEP. ACCT. 16-0331
PARKHURST & WENDEL

Sir:

The above-identified application was duly filed on June 26, 2003 without an executed Declaration. Accordingly, submitted herewith is a fully executed Declaration and Assignment of the inventors. Attached is our check for \$2,102.00, which includes \$130.00 to cover the payment of the fee in accordance with 37 CFR 1.16(e), \$40.00 to cover the fee for recordation of the Assignment document, and the remainder to cover the full filing fees.

08/08/2003 GWORDF1 00000033 10606283

| | |
|------------|------------|
| 01 FC:1001 | 750.00 OP |
| 02 FC:1201 | 84.00 OP |
| 03 FC:1202 | 1098.00 OP |
| 04 FC:1051 | 130.00 OP |

Entry of these documents should complete all of the filing formalities. Accordingly examination and allowance of the application in due course are respectfully solicited.

All correspondence should be sent to applicants' representative at the address indicated below.

The Commissioner is hereby authorized to charge any additional fee (or credit any overpayment) associated with this communication to Deposit Account No. 160331.

Respectfully submitted,

PARKHURST & WENDEL, L.L.P.

August 7, 2003

Date

RWP:klb

Attorney Docket No. HYAE:165

PARKHURST & WENDEL, L.L.P.
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(rev. 1/03)



Roger W. Parkhurst
Registration No. 25,177



TRADEMARK
Evidence, post office address and citizenship are as stated below next to my name; that

1 SEMICONDUCTOR INTEGRATED CIRCUIT DESIGNING APPARATUS, SEMICONDUCTOR INTEGRATED CIRCUIT

described and claimed in the specification: DESIGNING METHOD, SEMICONDUCTOR INTEGRATED CIRCUIT

Check one

*a. [] attached hereto.

b. ☒ filed on June 26, 2003 as Application Serial No. 10/606,283 and amended on _____;
(if applicable)

I hereby state that I have reviewed and understand the contents of the above-identified application, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information of which I am aware which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, §1.56(a). Under Title 35 U.S. Code §119, the priority benefits of the following foreign application(s) filed within one year prior to this application are hereby claimed:

Japanese Patent Application No. 2002-188421 filed June 27, 2002

The following applications for patent or inventor's certificate on this invention were filed in countries foreign to the United States of American either (a) more than one year prior to this application, or (b) before the filing date of the above-named foreign priority application(s):

2 If there are no corresponding applications, insert "NONE".

I hereby appoint the following as my attorneys of record with full power of substitution and revocation to prosecute this application and to transact all business in the Patent Office:

Roger W. Parkhurst, Reg. No. 25,177; and/or Charles A. Wendel, Reg. No. 24,453.

**ALL CORRESPONDENCE IN CONNECTION WITH THIS APPLICATION SHOULD BE SENT
TO PARKHURST & WENDEL, L.L.P., 1421 Prince Street, Suite 210, Alexandria, Virginia 22314-2805
Telephone: (703) 739-0220.**

I hereby declare that I have reviewed and understand the contents of this Declaration, and that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

3 Typewritten Full Name of
Sole or First Inventor

Noriko

SHINOMIYA

Given Name

Middle Initial

Family Name

***4 Inventor's Signature**

Noriko

Shinomiya

5 Date of Signature

July

16

2003

Month

Day

Year

6 Residence

Ibaraki-shi

Japan

City

State or Province

Country

7 Citizenship

Japanese

8

Post Office Address

1-8-22-311, Tennoh, Ibaraki-shi, Osaka 567-0876 JAPAN

(Insert complete mailing

address, including country)

*This form may be executed only when attached to the specification (including claims) at the end thereof if Box a. is checked.

****Note to the Inventor.** Please sign name on line 4 exactly as it appears in line 3 and insert the actual date of signing on line 5.

IF THERE IS MORE THAN ONE INVENTOR USE PAGE 2 AND PLACE AN "X" HERE ☐